

JC5/705

6	Subclass	ISSUE CLASSIFICATION		PATENT NUMBER	

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

A/A

APPLICATION NO. 09/825027	CONT/PRIOR	CLASS 324 257	SUBCLASS 48	ART UNIT 2858	EXAMINER Kolter
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2829

TITLE
 Clock-based method and devices for measuring voltage-variable
 capacitances and other on-chip parameters

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____. <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Assistant Examiner) _____ (Date) _____ (Primary Examiner) _____ (Date) _____ (Legal Instruments Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED ISSUE FEE Amount Due _____ Date Paid _____ ISSUE BATCH NUMBER	
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